

Fig. 1

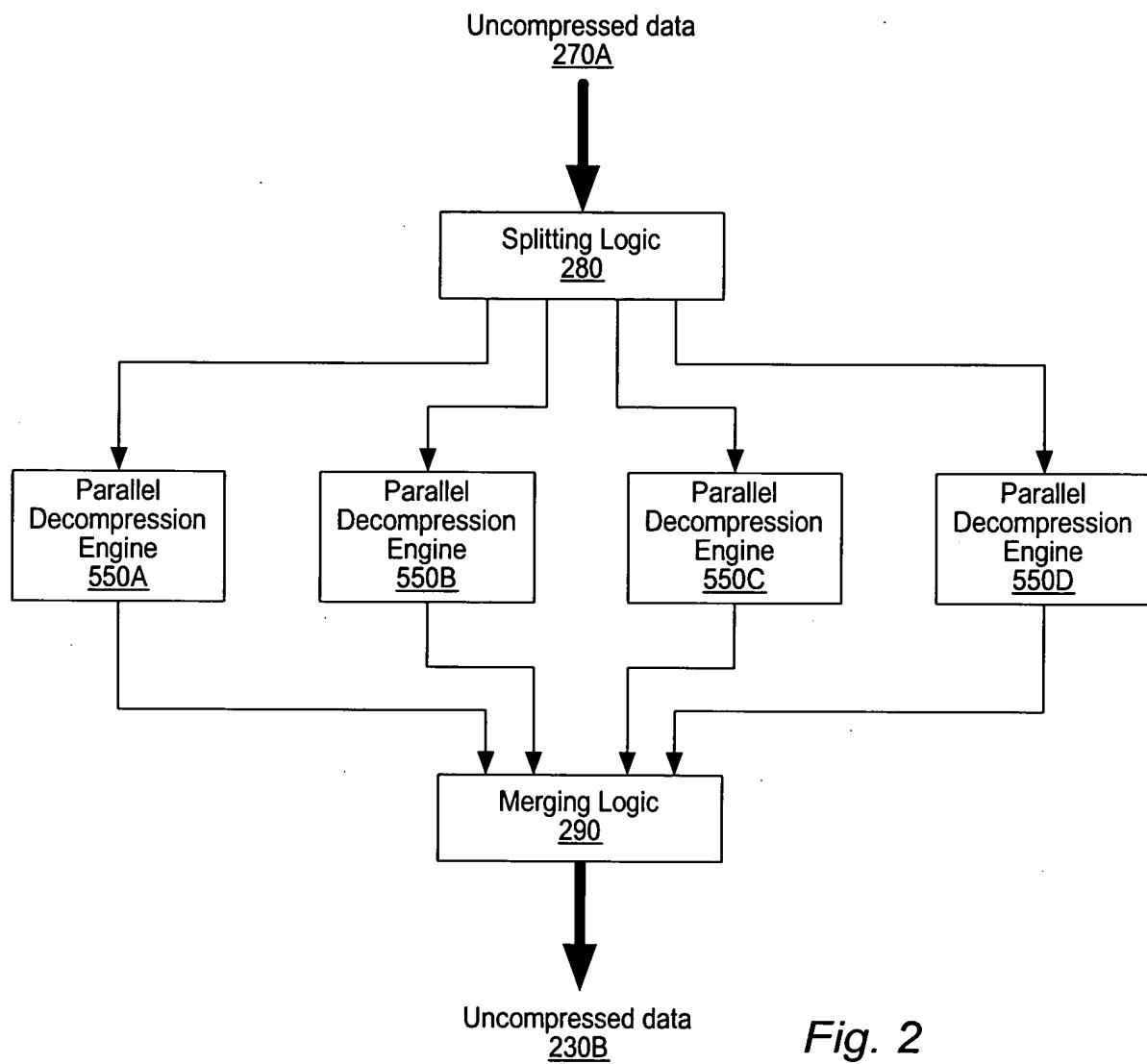


Fig. 2

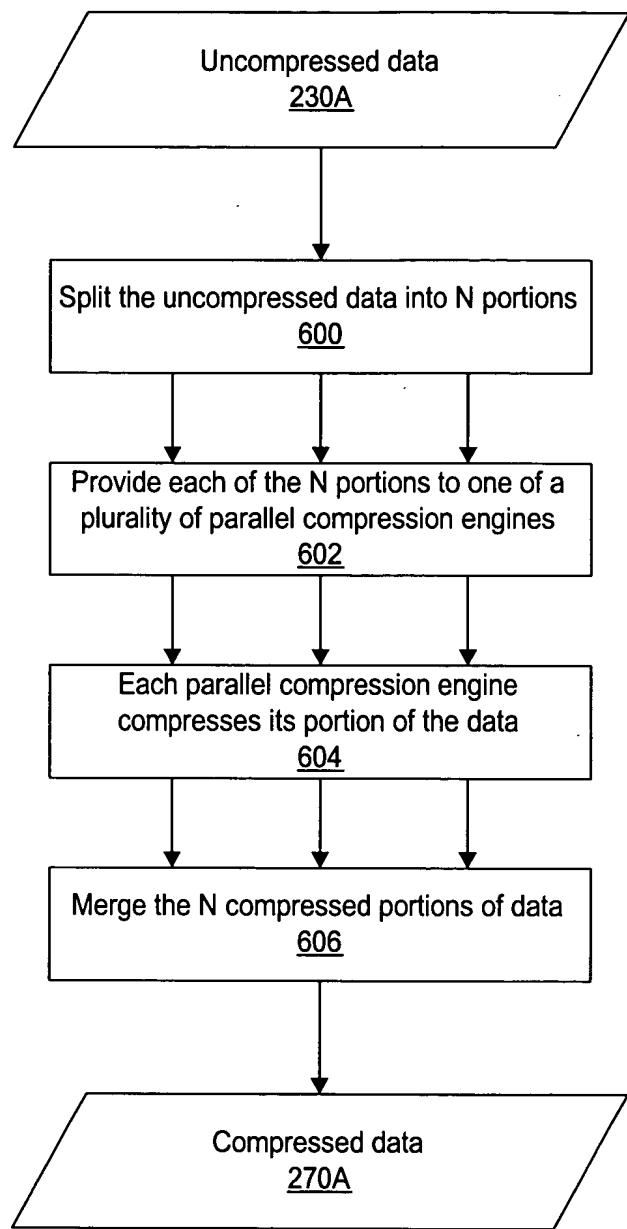


Fig. 3

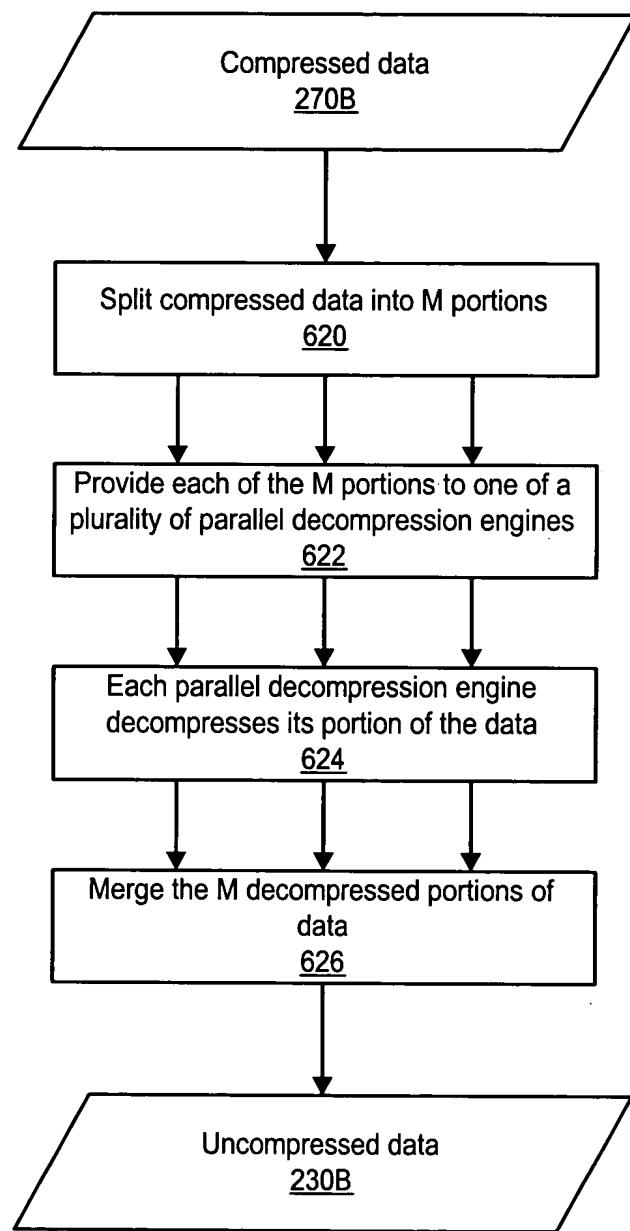


Fig. 4

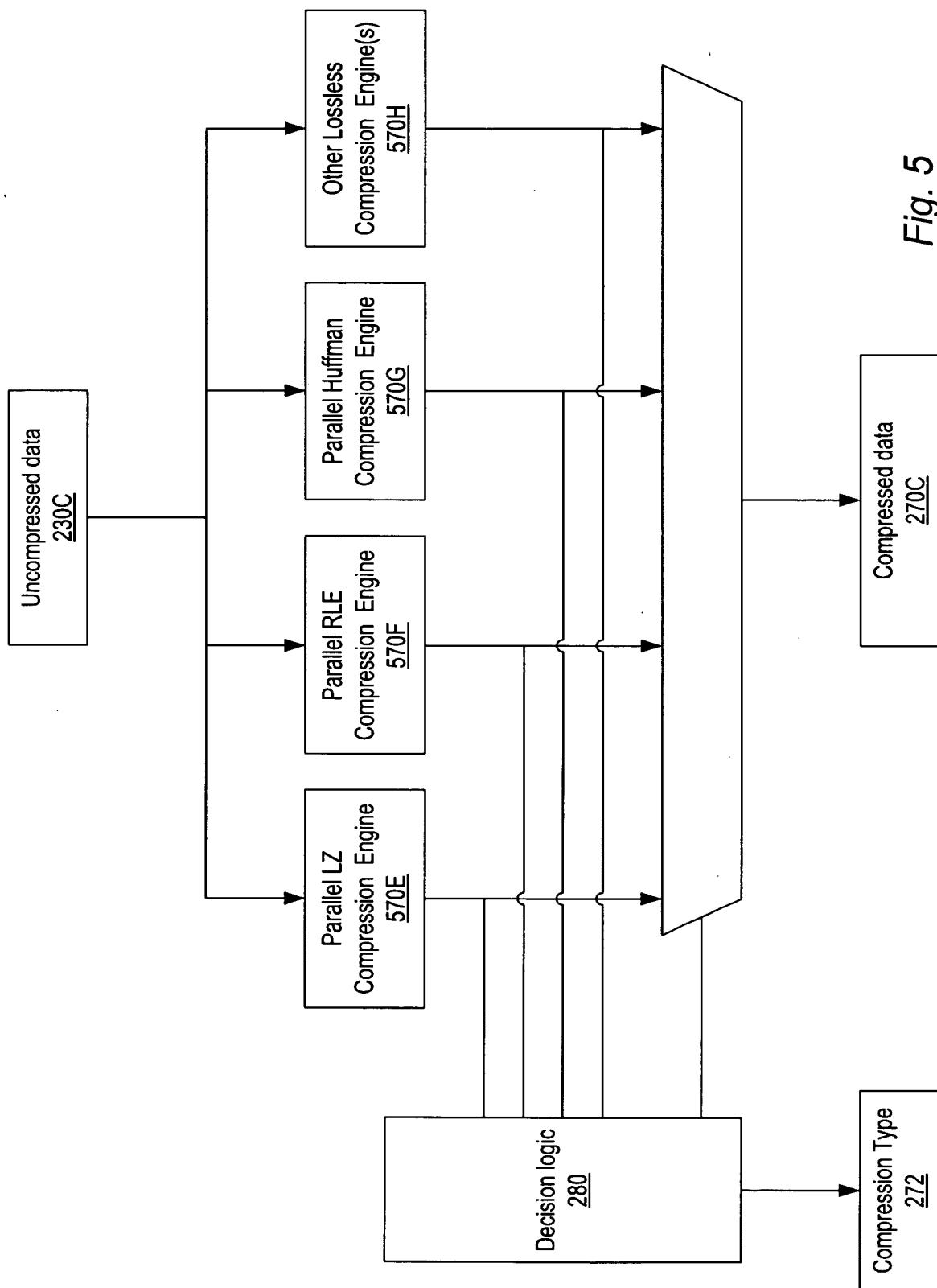
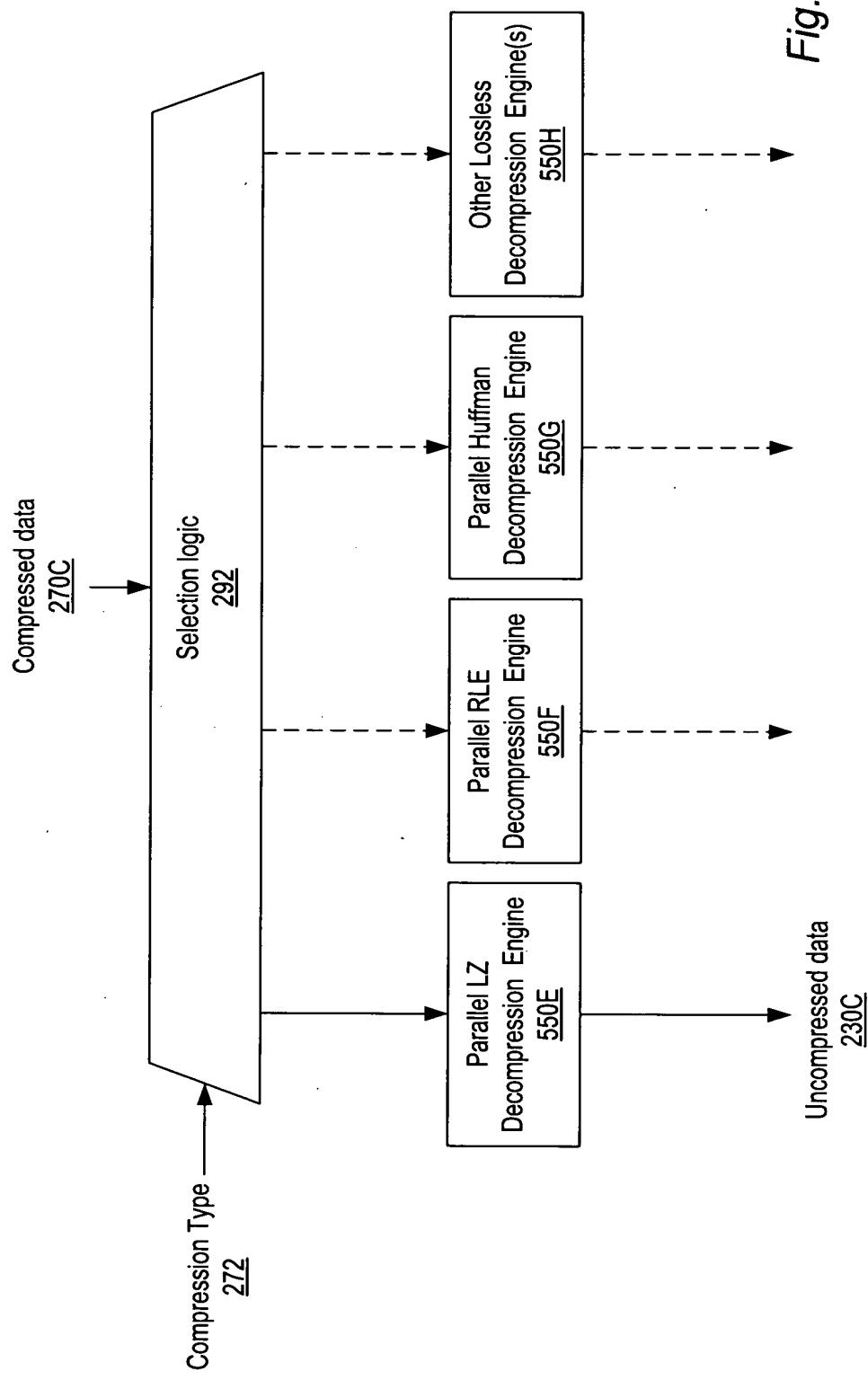


Fig. 5

Fig. 6



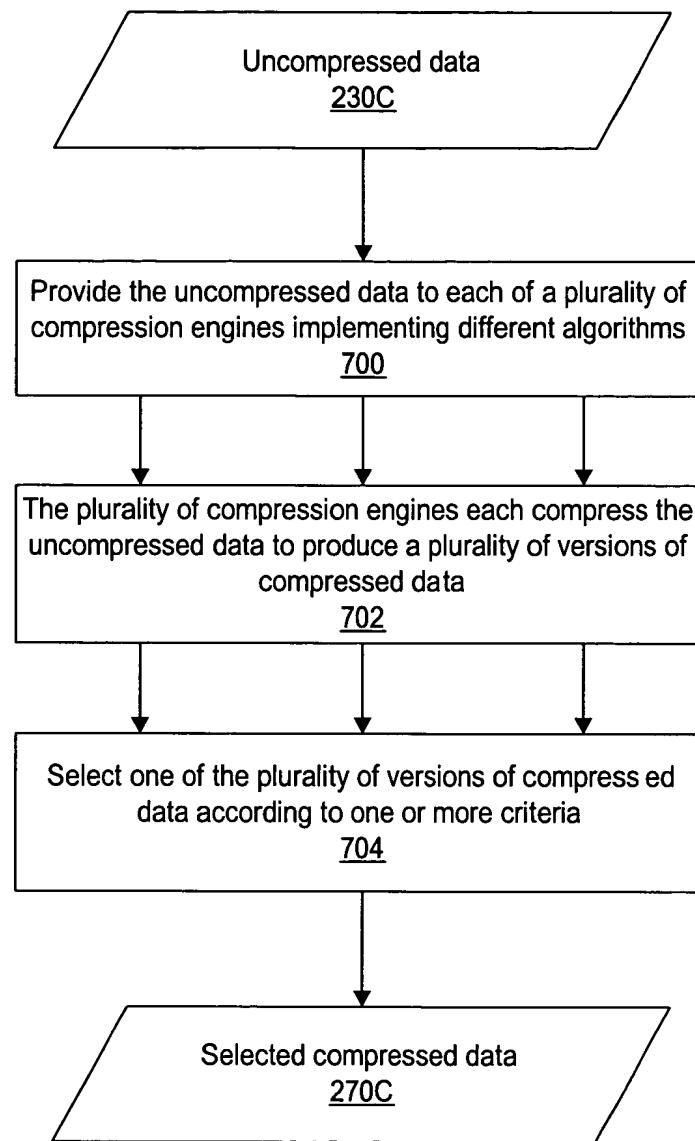


Fig. 7

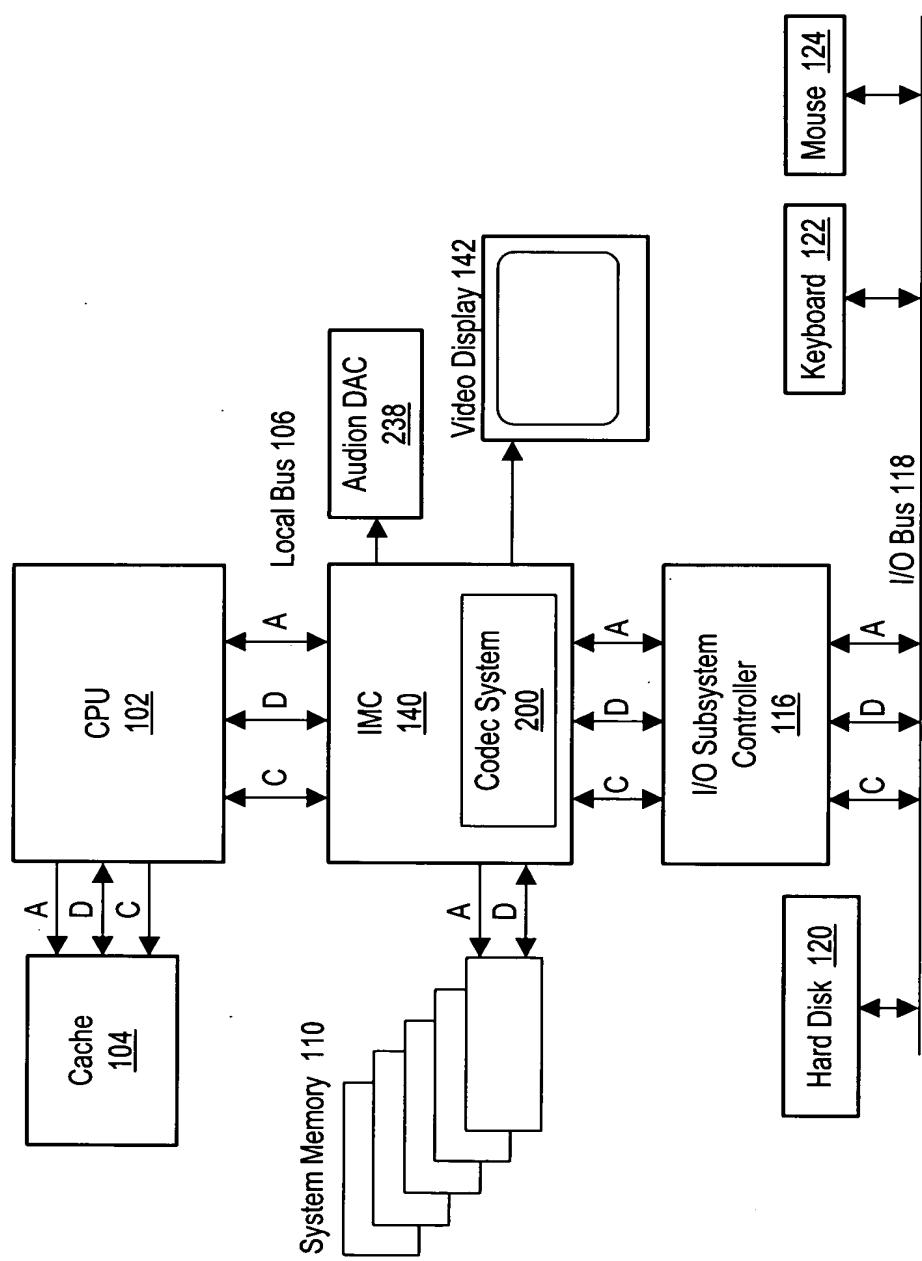


Fig. 8a

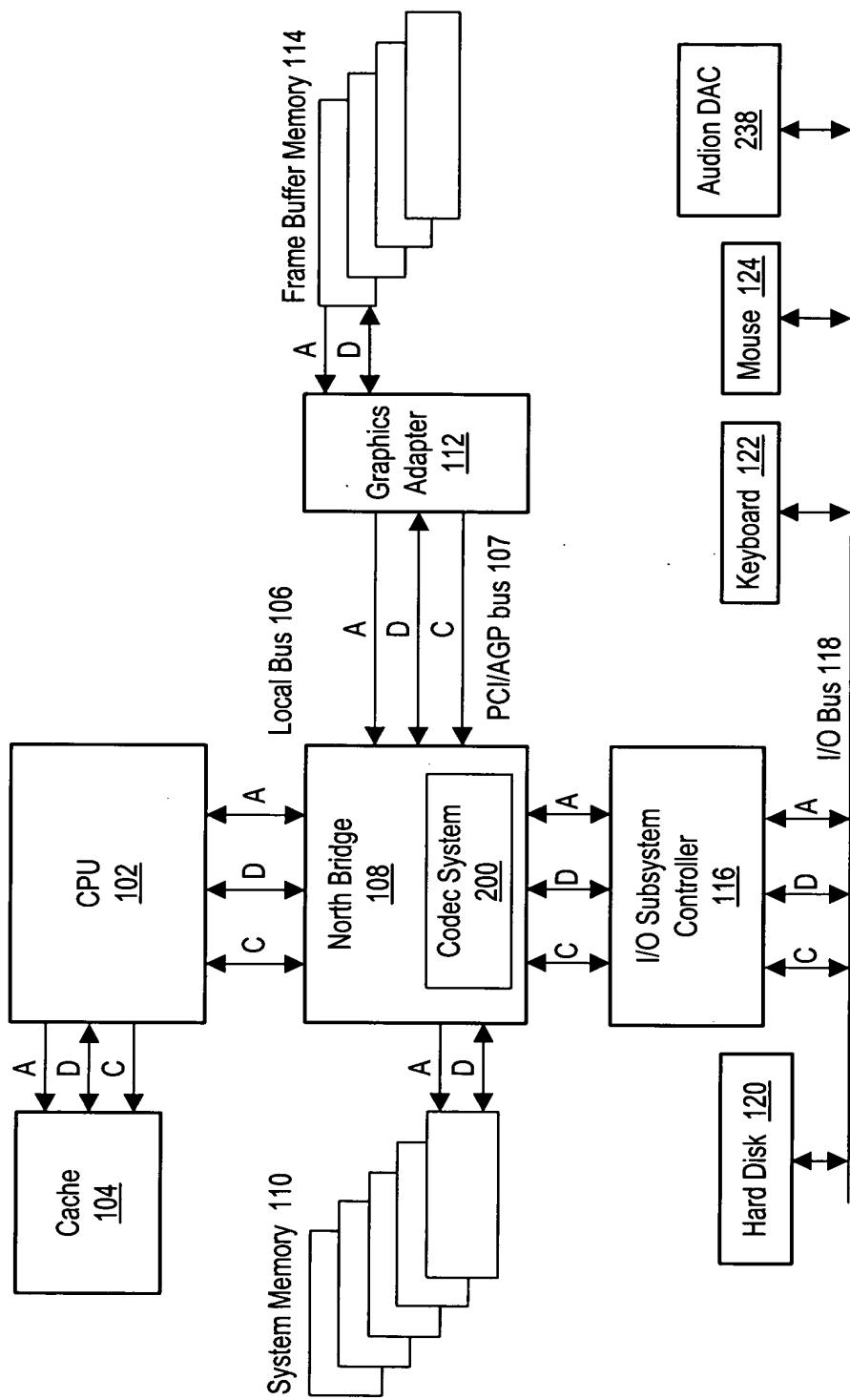


Fig. 8b

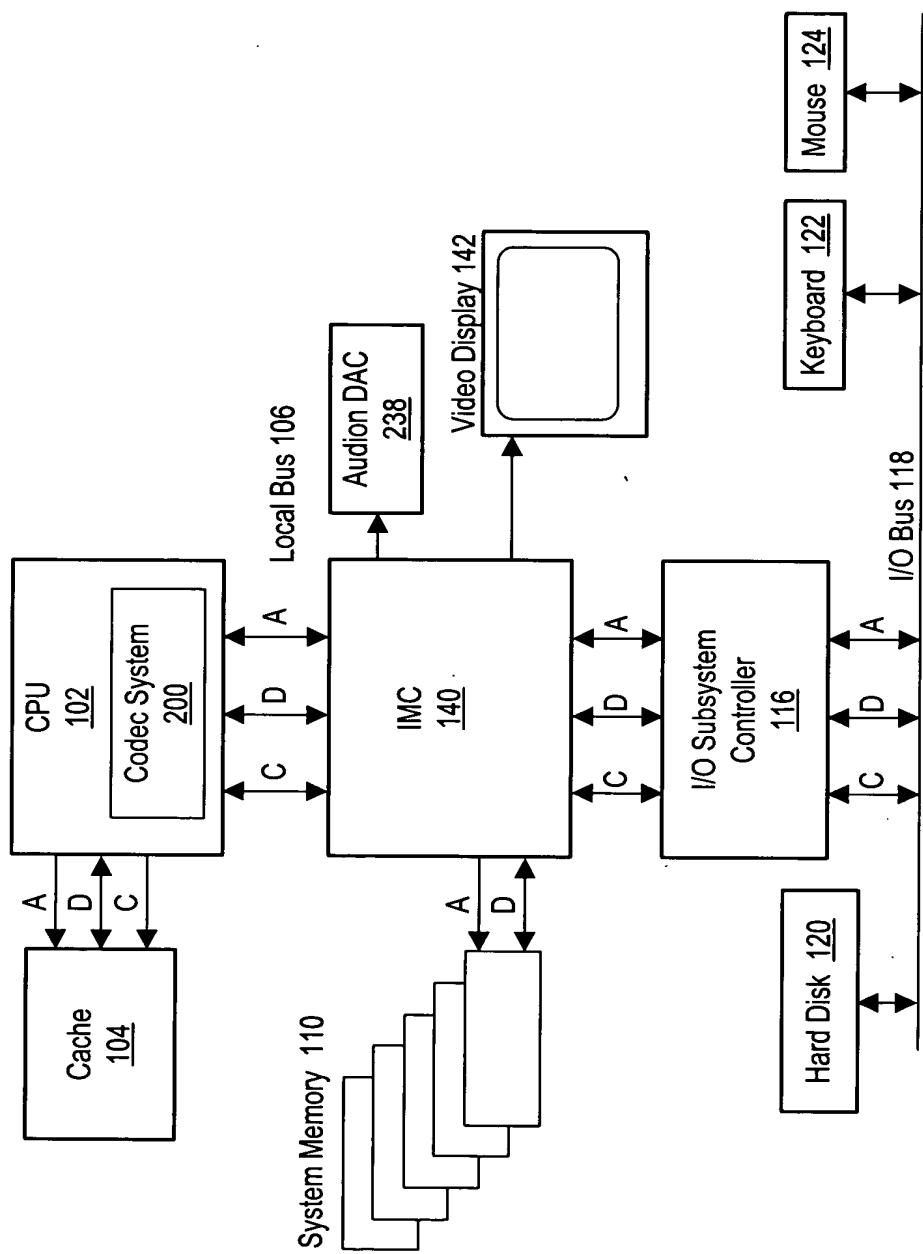


Fig. 8c

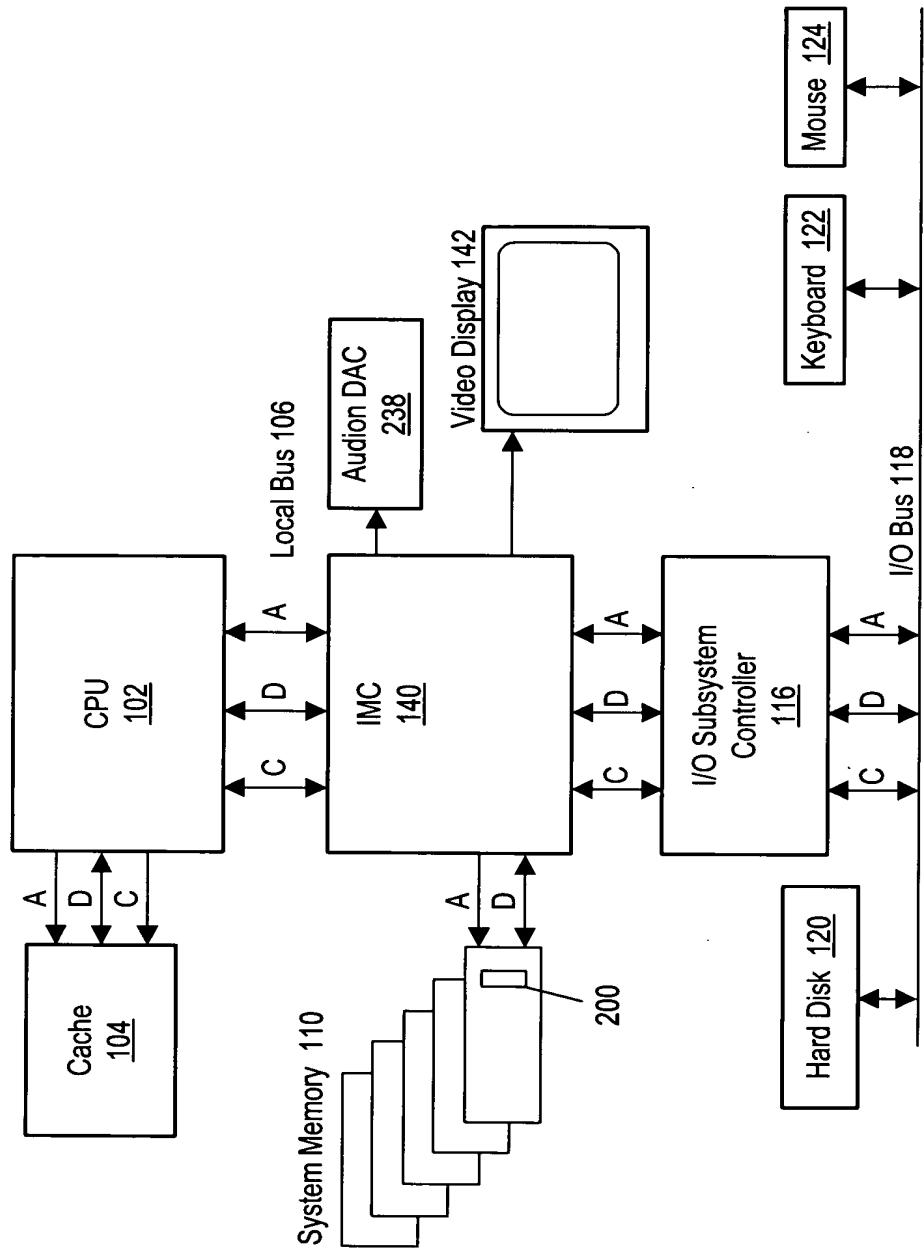


Fig. 8d

Diagram illustrating the system architecture and memory hierarchy.

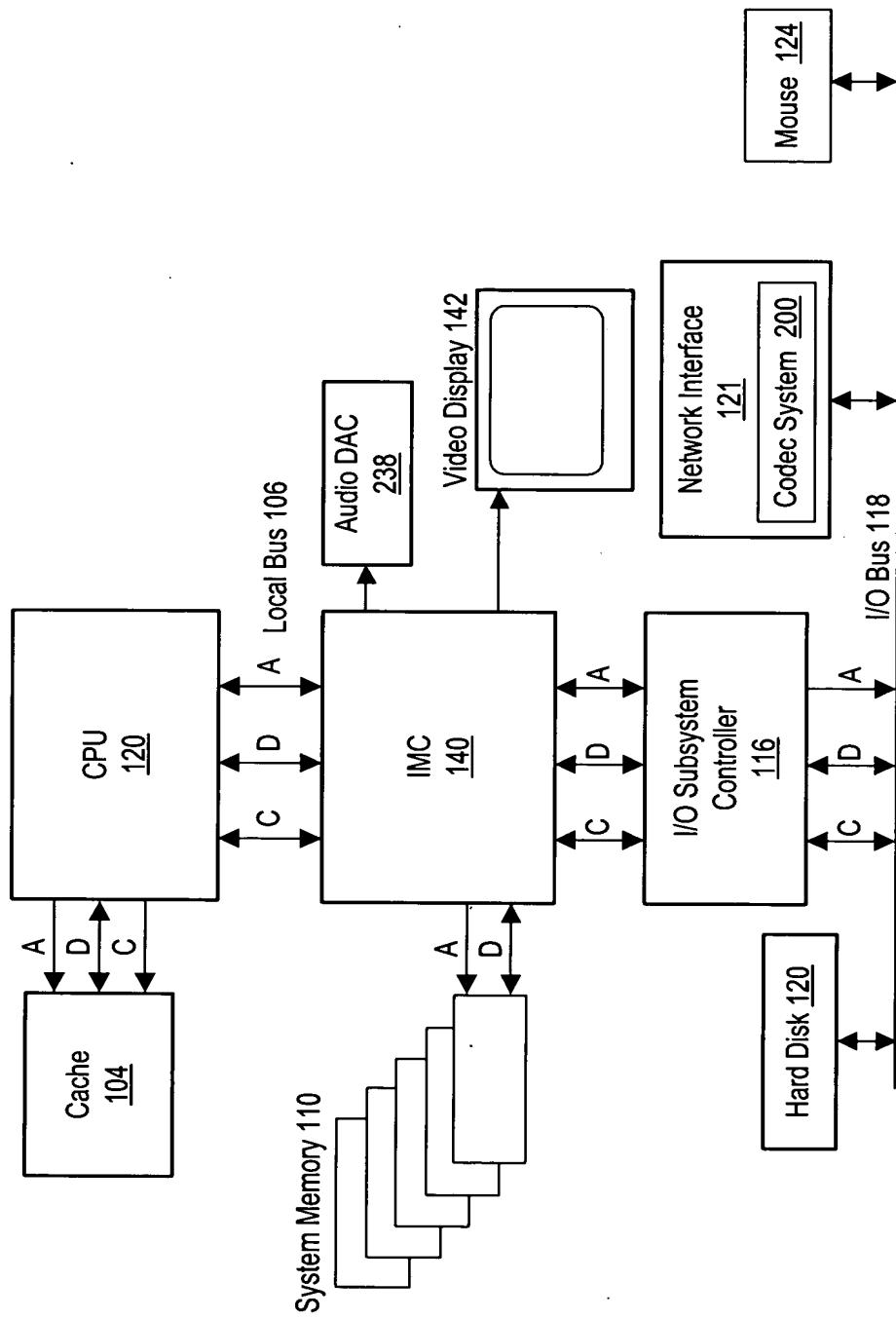


Fig. 8e

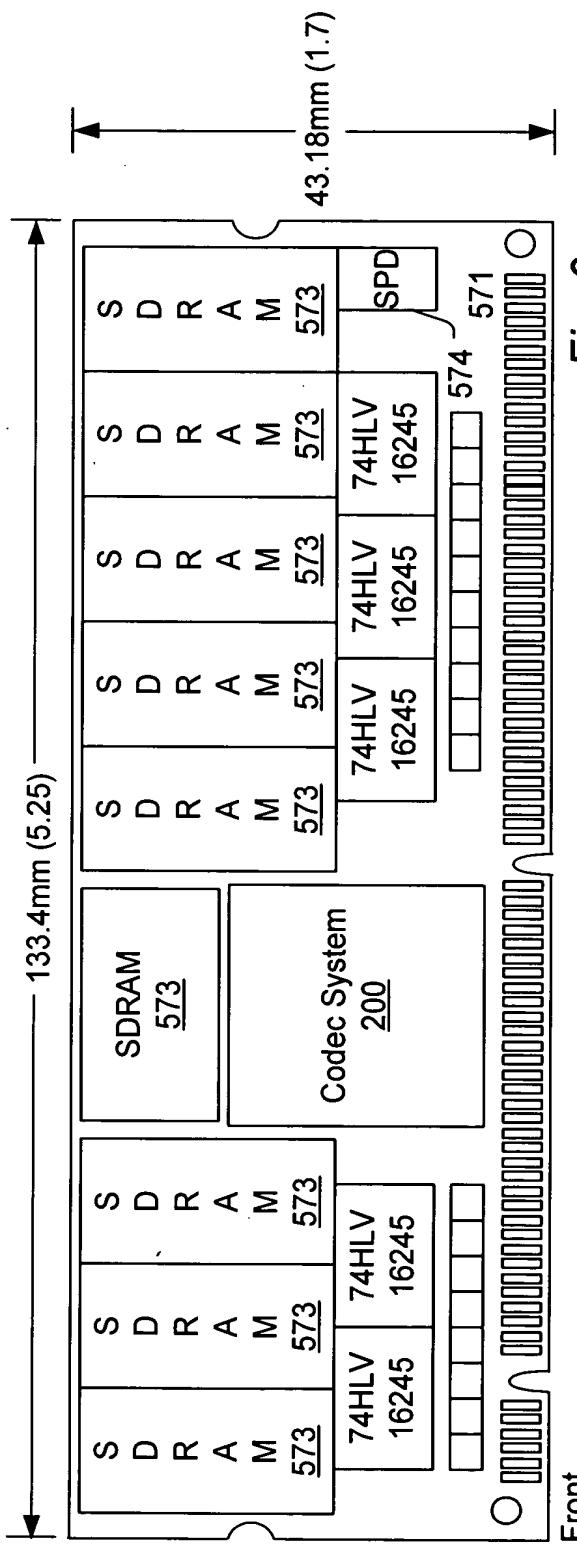


Fig. 9a

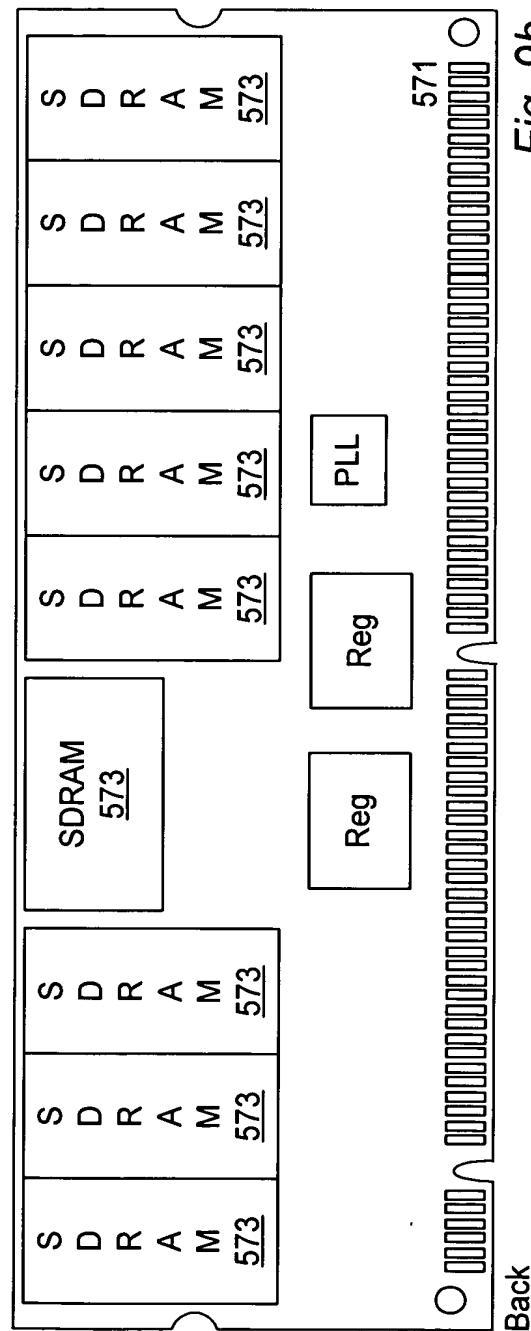
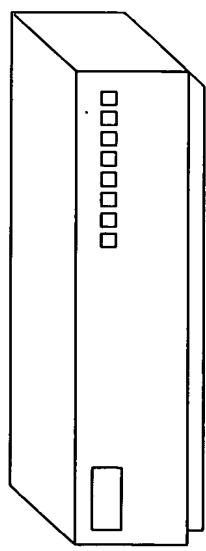
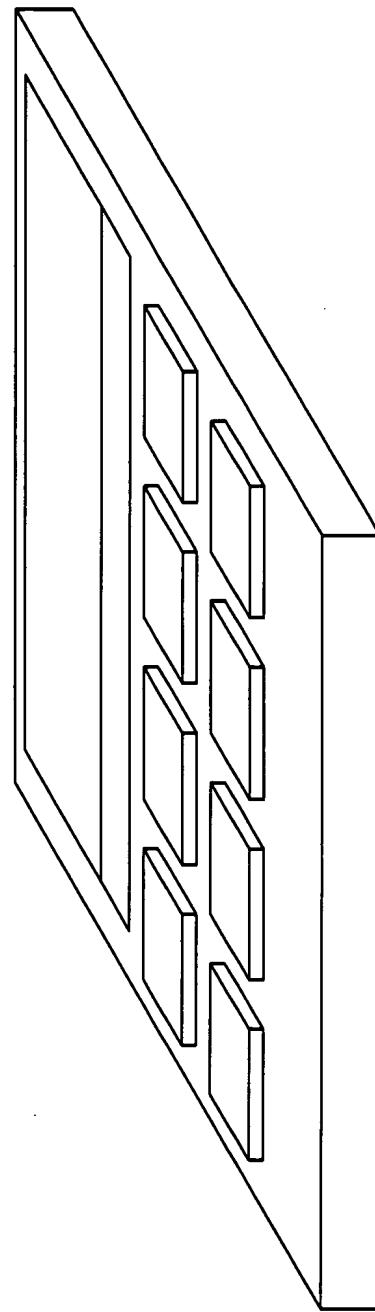


Fig. 9b



Network Device 130

Fig. 10



PDA

Fig. 11

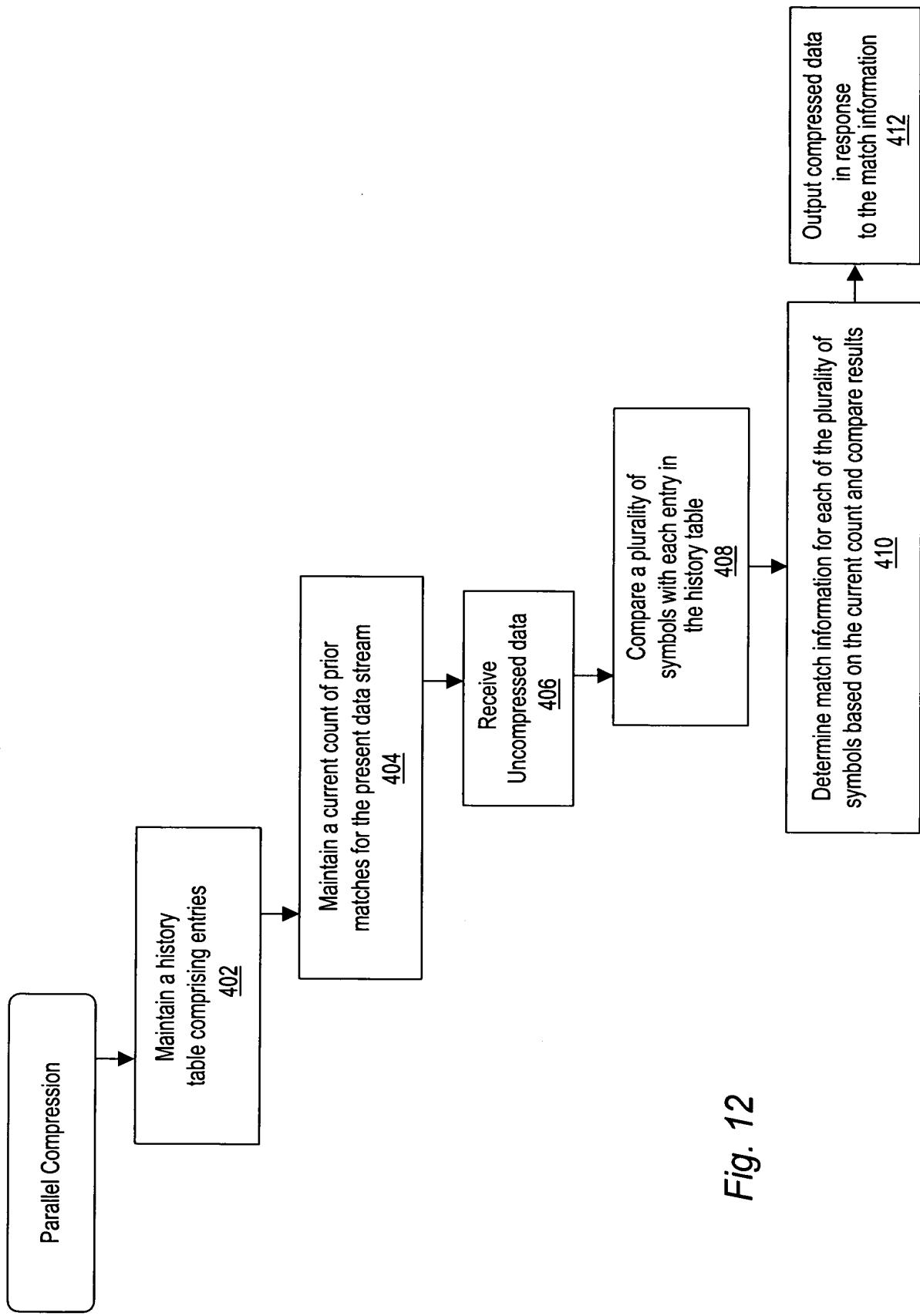


Fig. 12

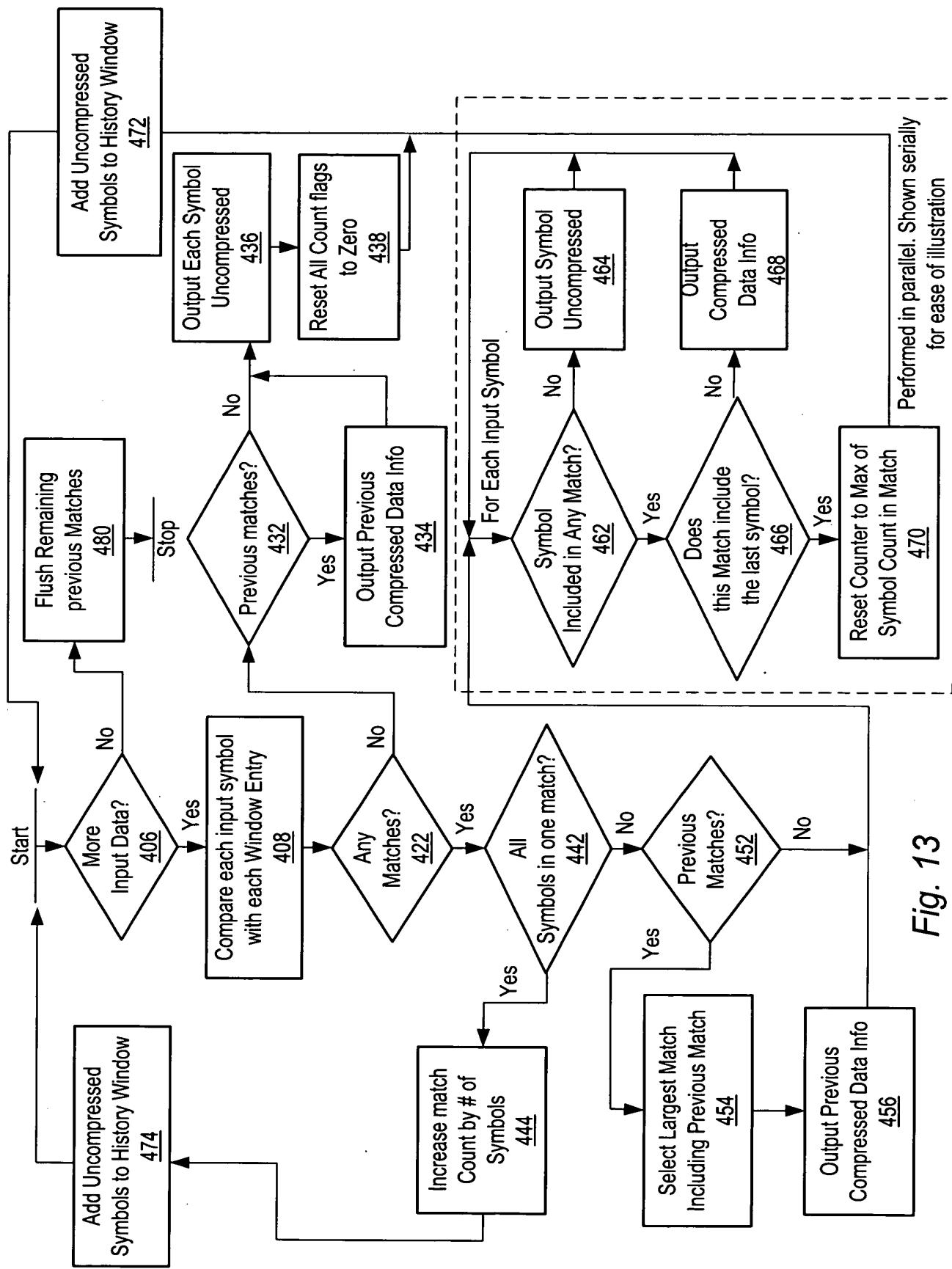


Fig. 13

Bytes Compressed	Flag	Index	Count	Data	Bits Used
0	0	-	-	8b	9
1	10	6b	-	-	8
2	1100	6b	-	-	10
3	1101	6b	-	-	10
4	1110	6b	-	-	10
5	1111000	6b	-	-	13
6	1111001	6b	-	-	13
7	1111010	6b	-	-	13
8	1111011	6b	-	-	13
9	1111100	6b	-	-	13
10	1111101	6b	-	-	13
11	1111110	6b	-	-	13
>11	1111111	6b	12b	-	25

Fig. 14

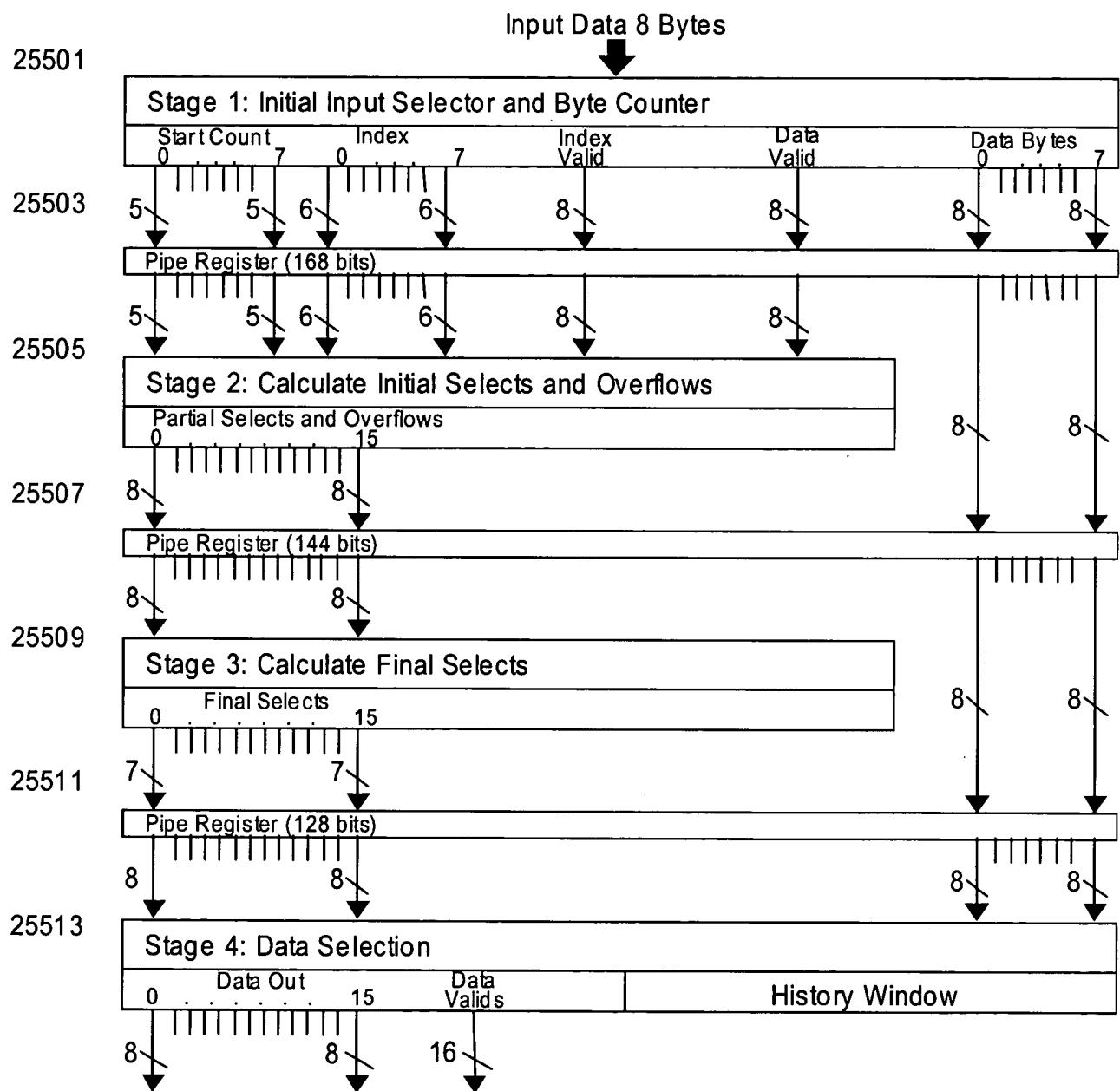


Fig. 15

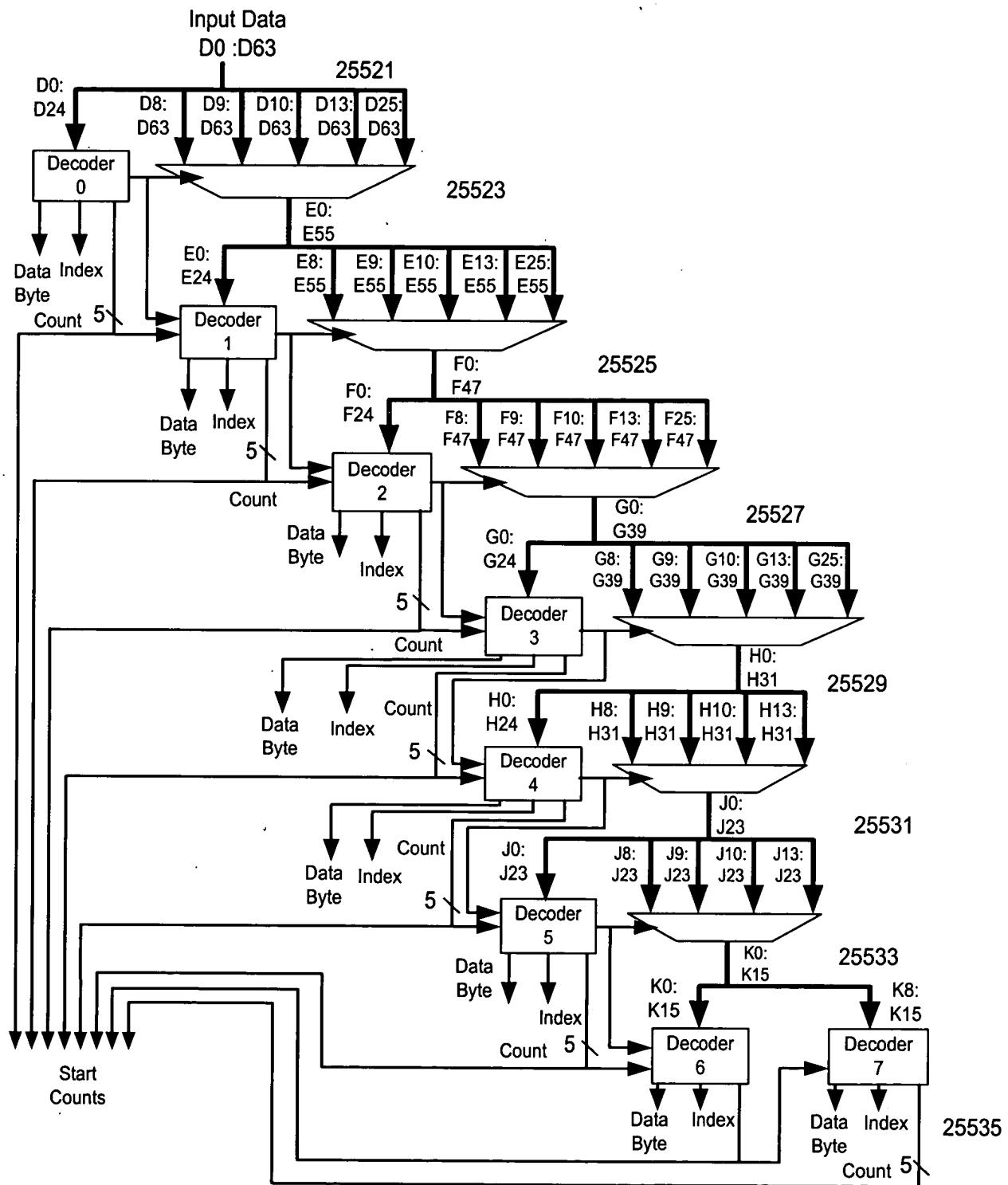


Fig. 16

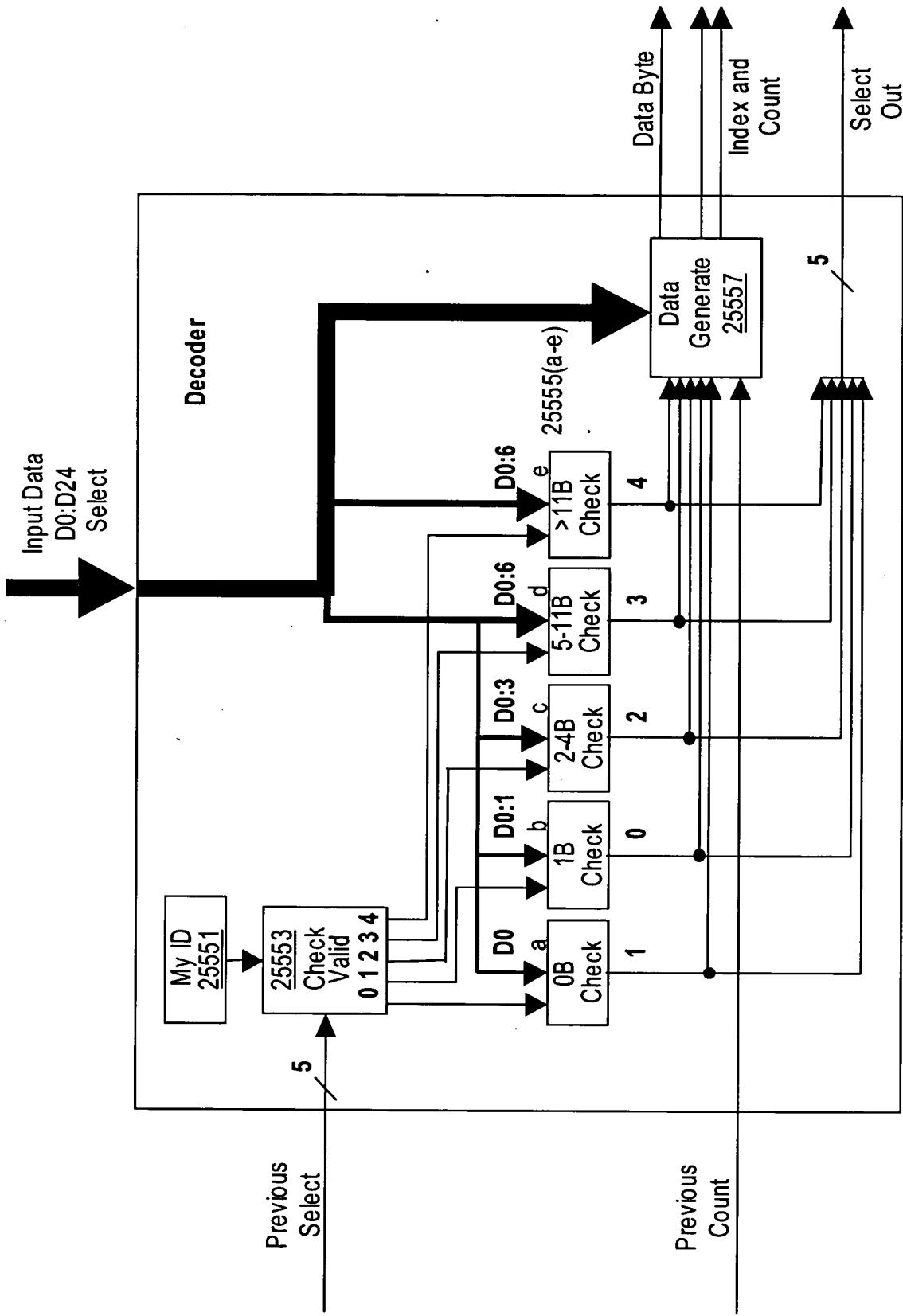


Fig. 17

Stage 2 - Calculate Selects & Overflows

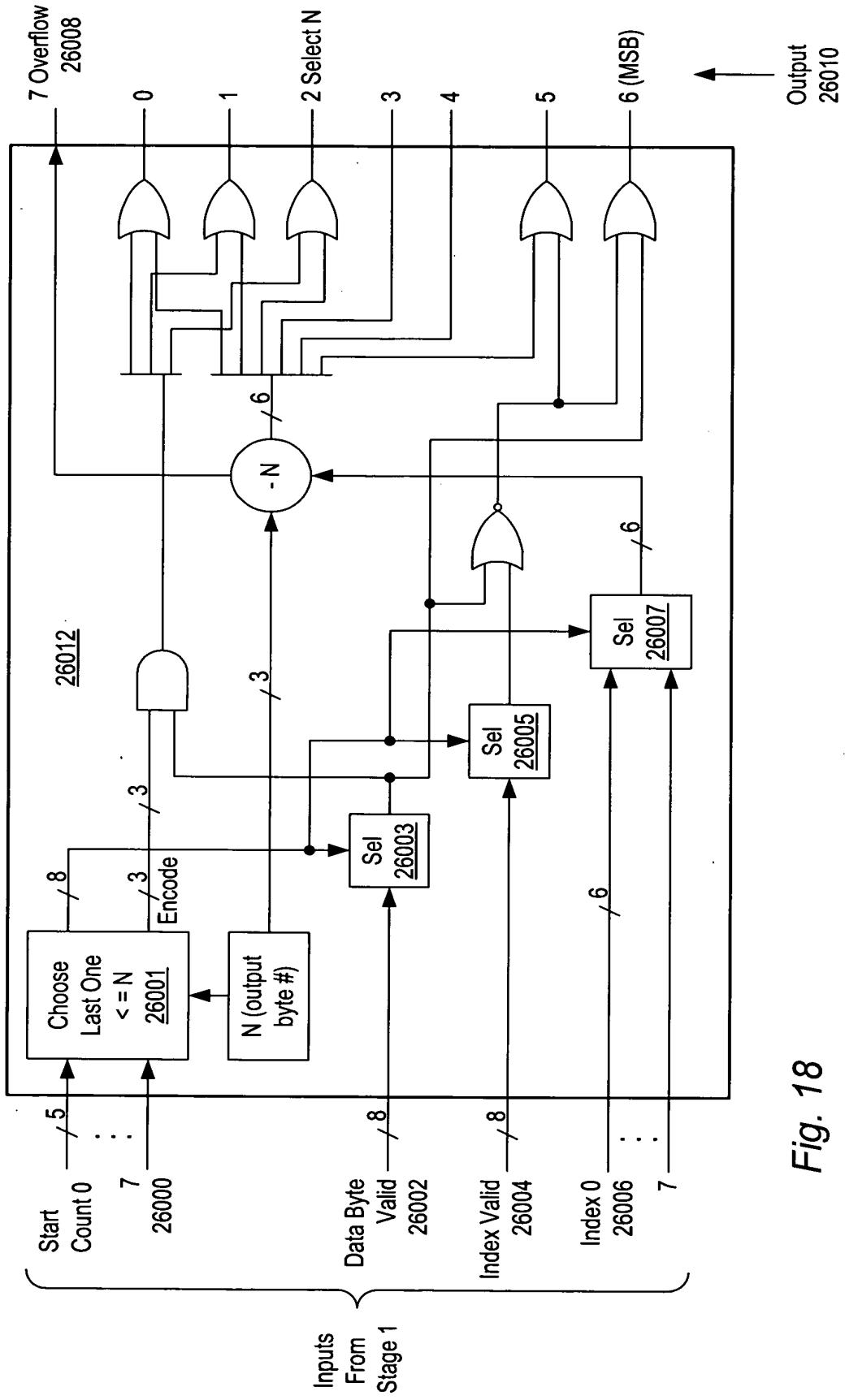


Fig. 18

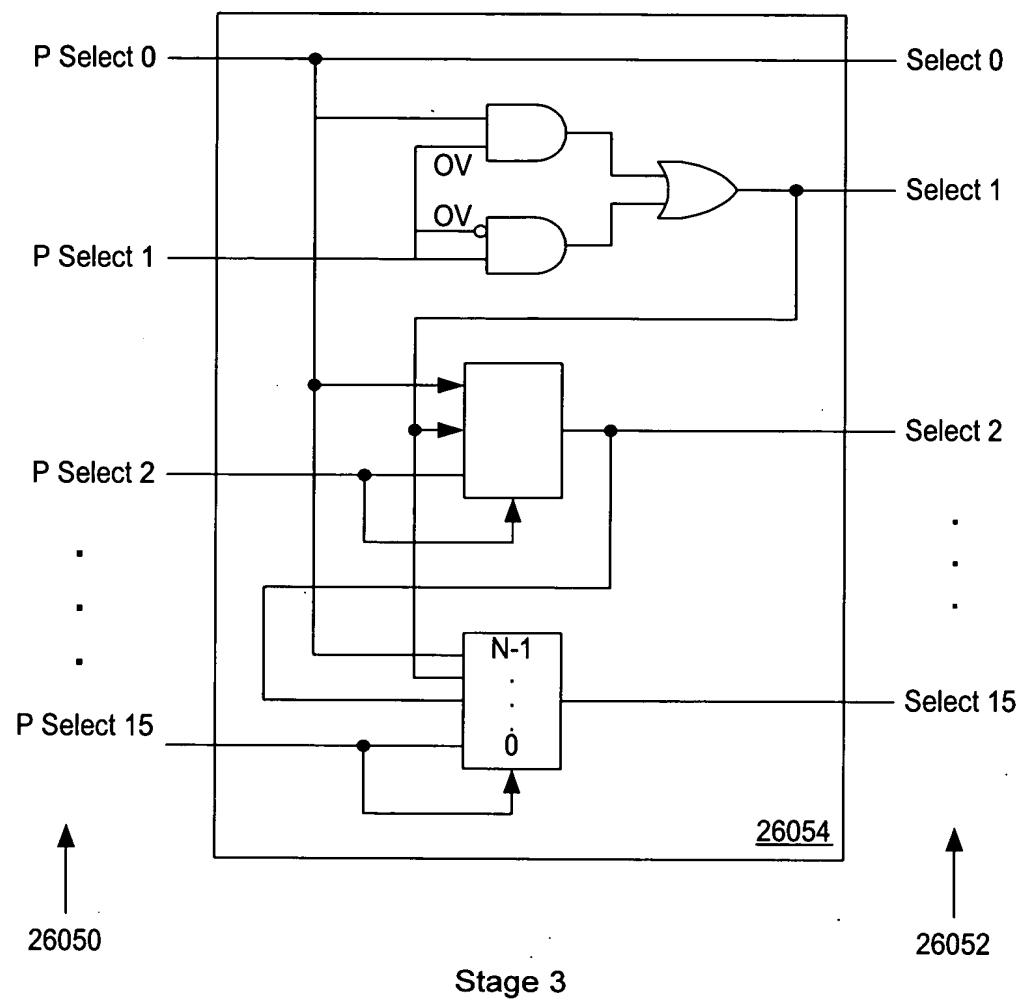


Fig. 19

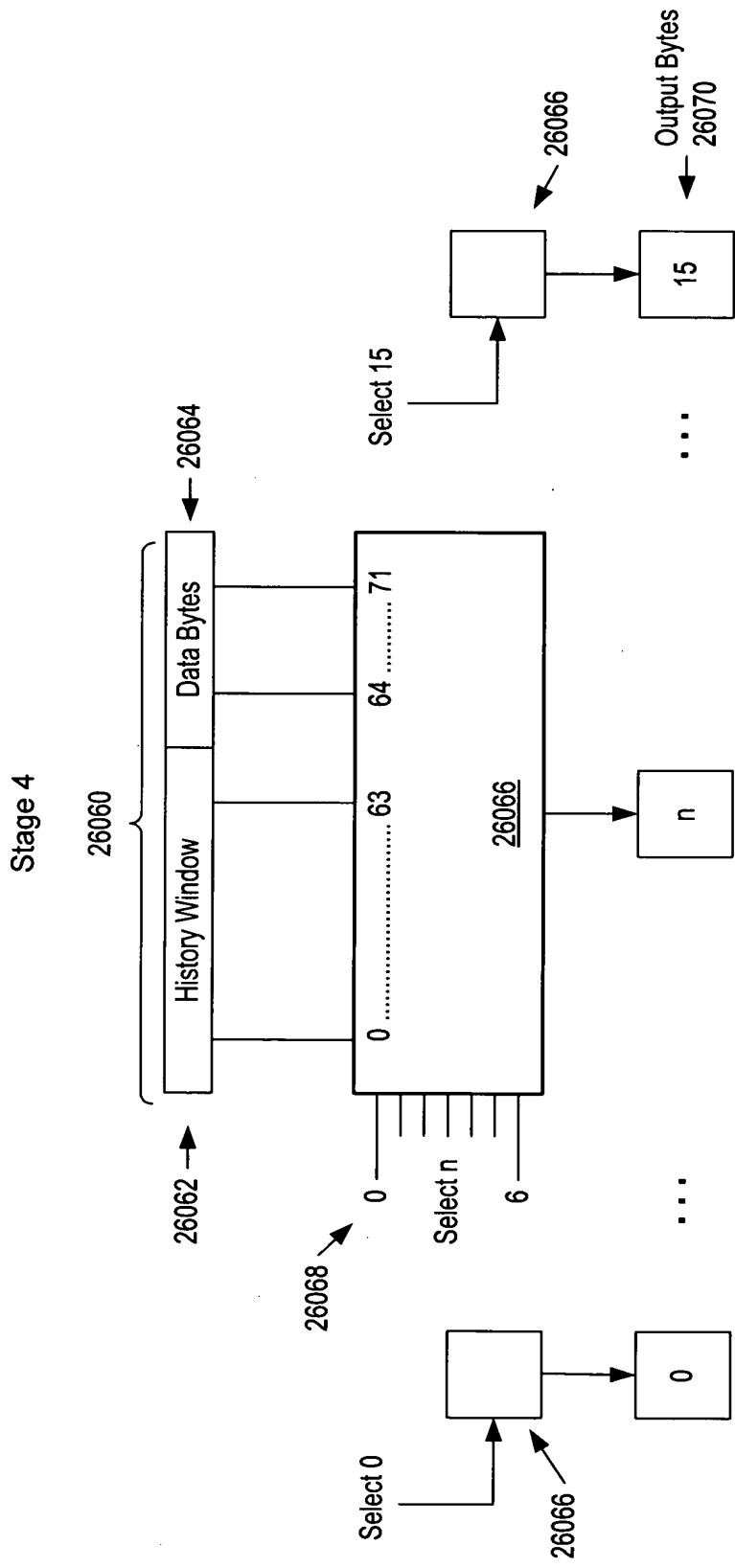


Fig. 20

Decompression data flow

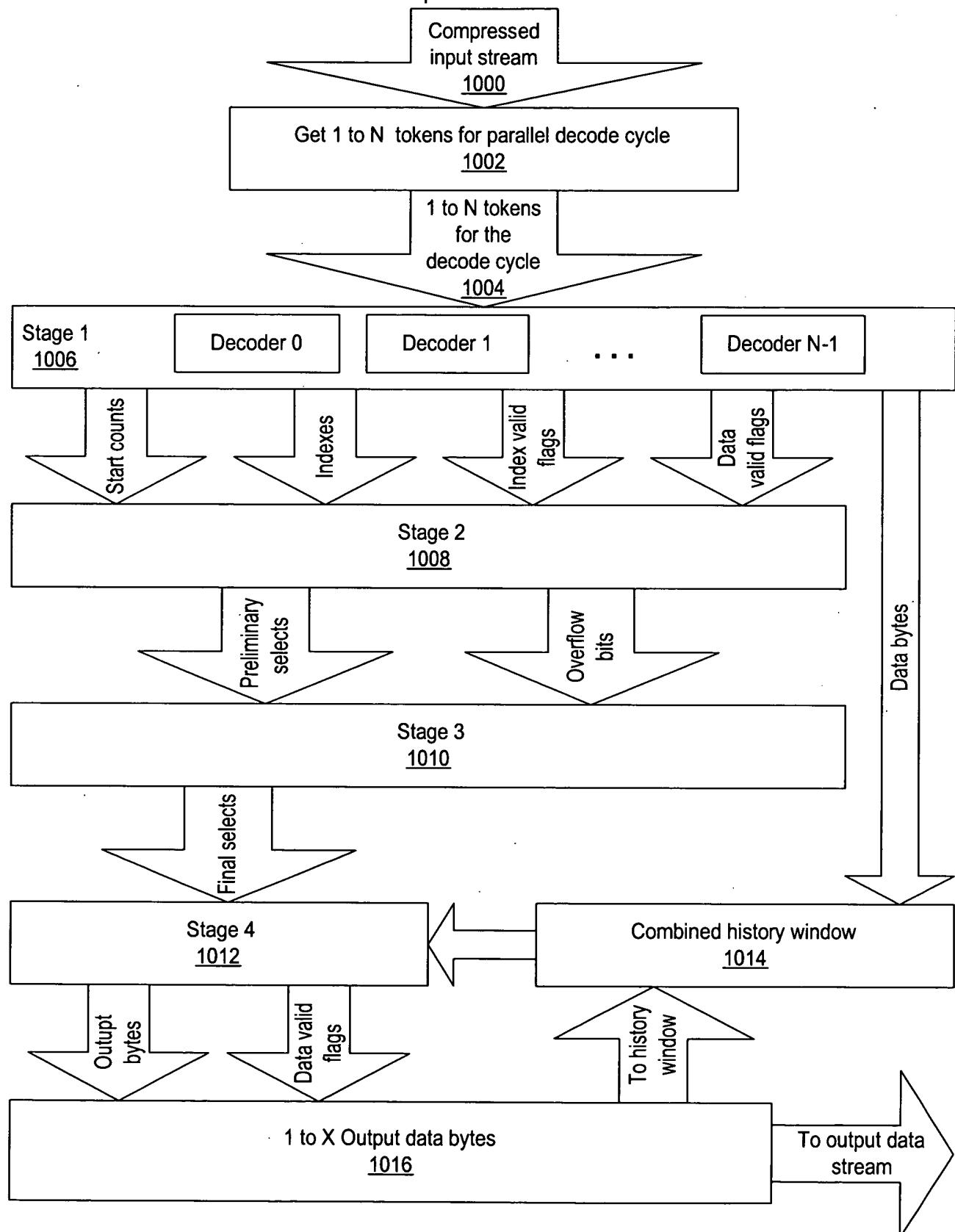
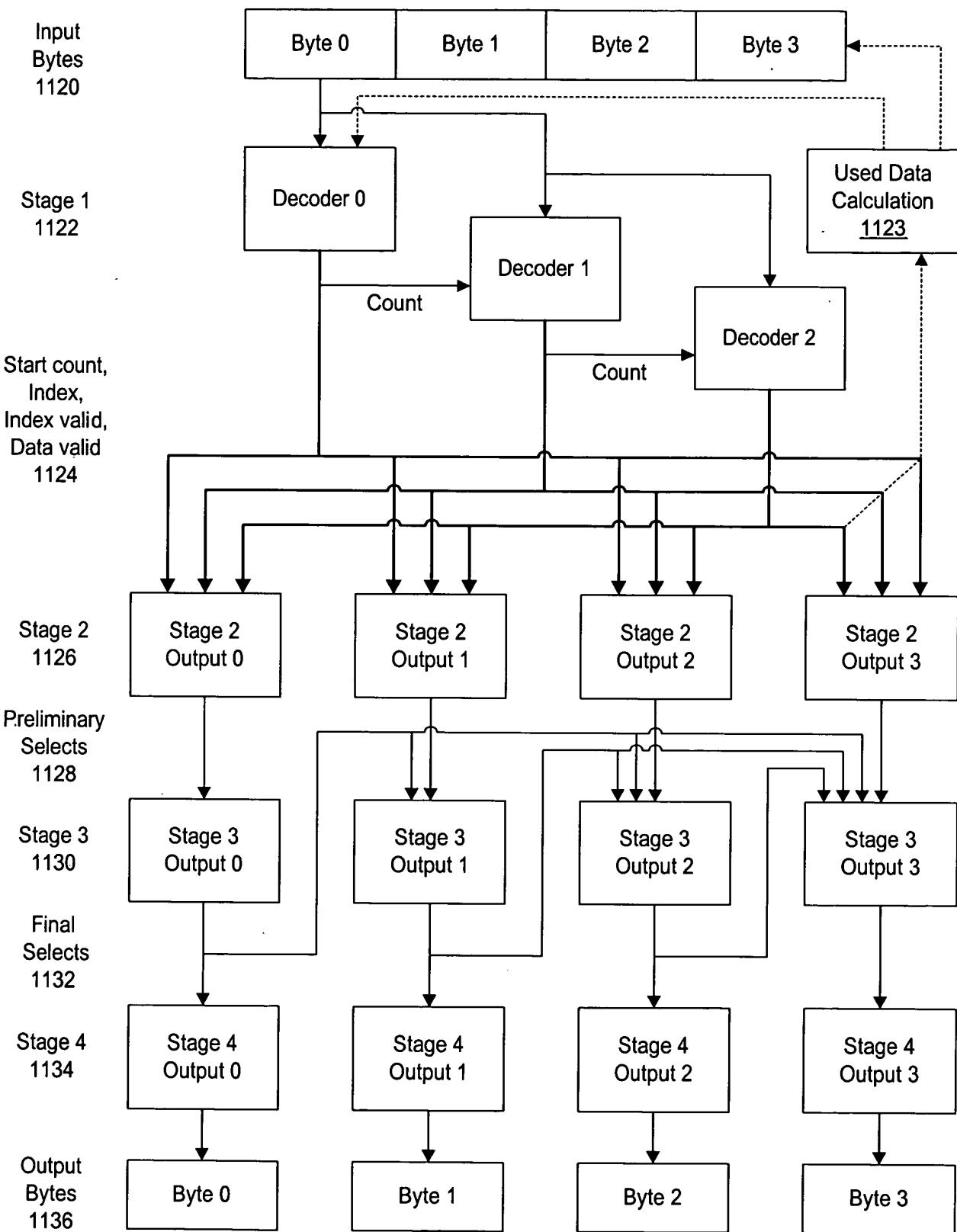
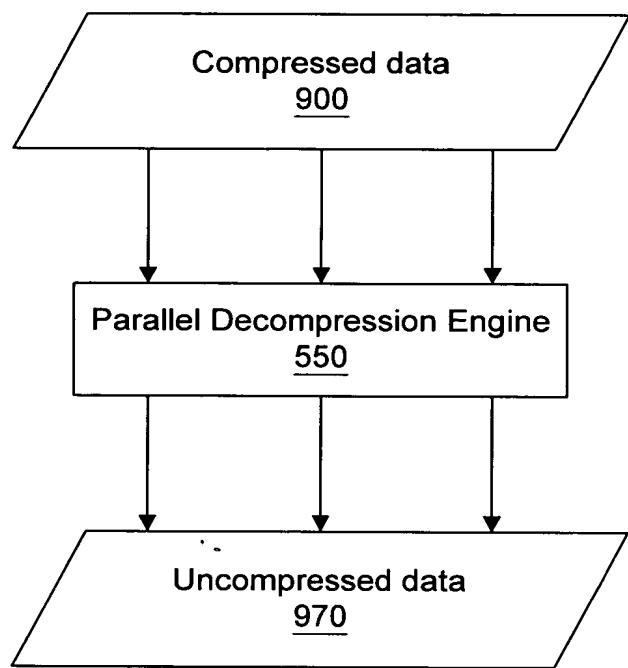


Fig. 21



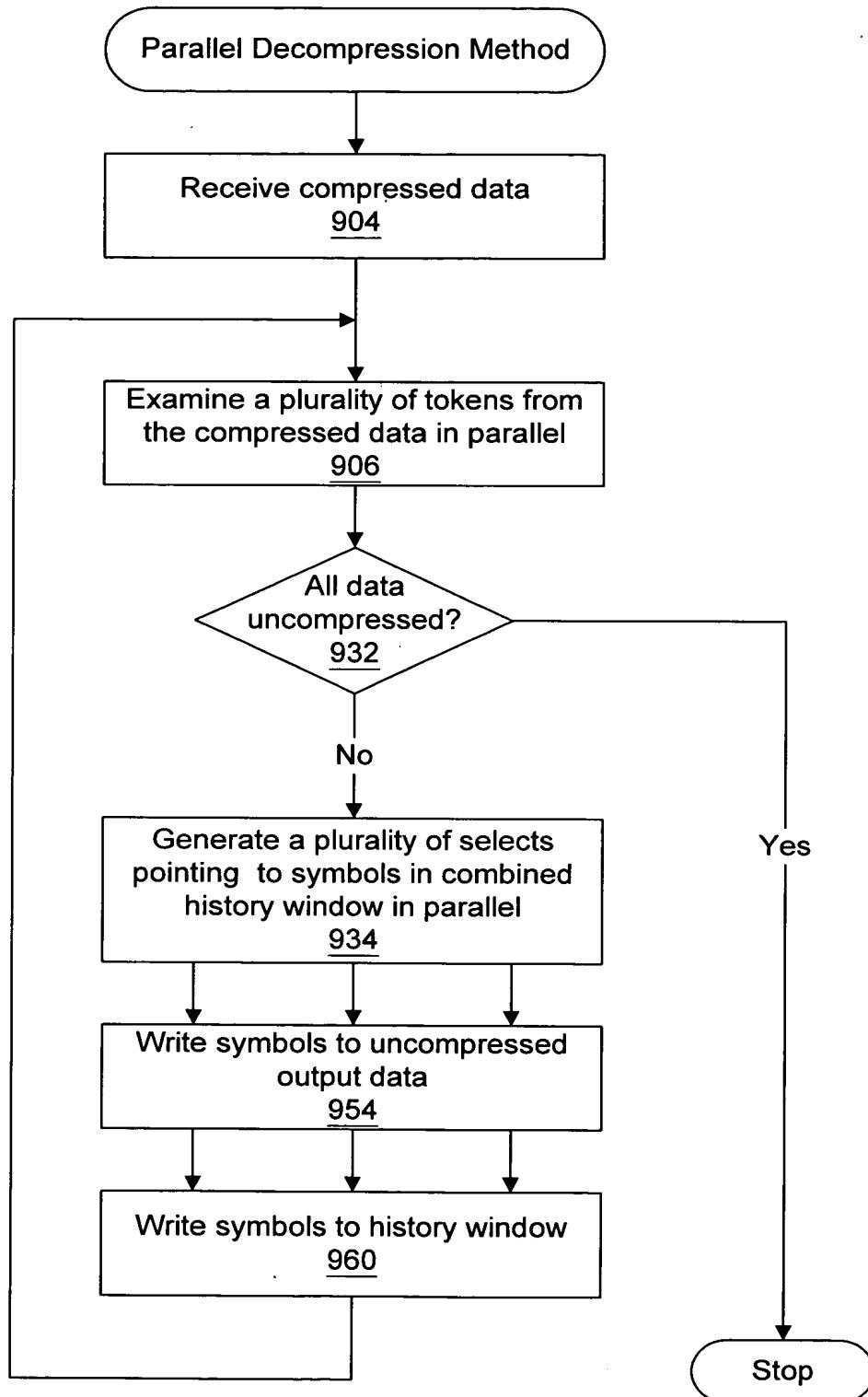
- 4 input bytes, 3 decoders, 4 output bytes

Fig. 22



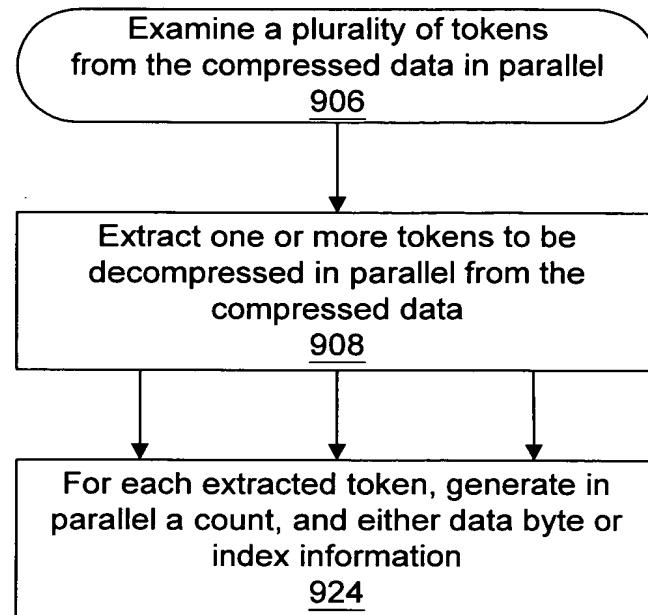
Decompression flowchart

Fig. 23a



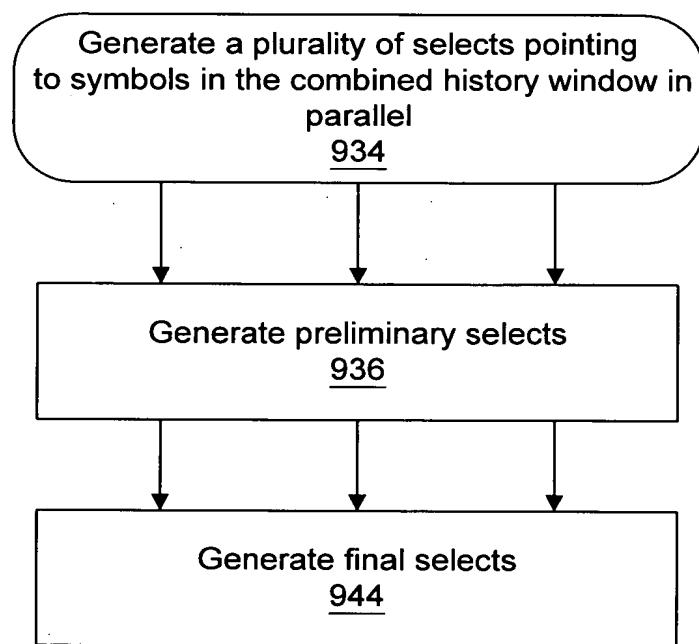
Decompression flowchart

Fig. 23b



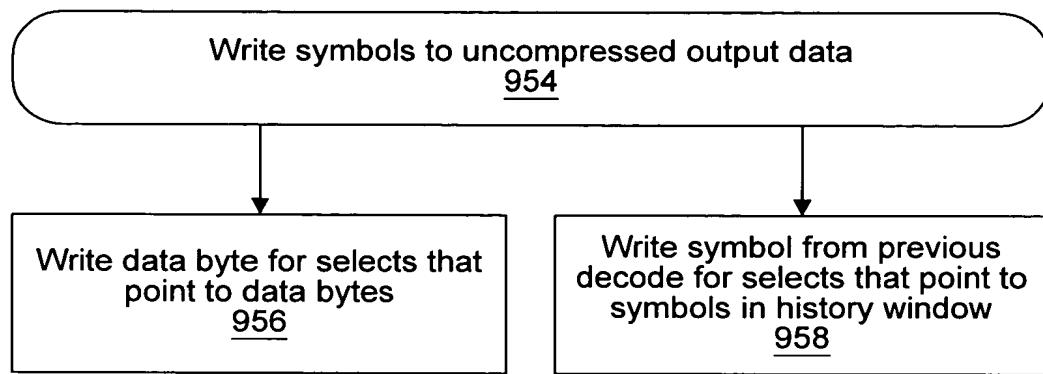
Decompression flowchart

Fig. 23c

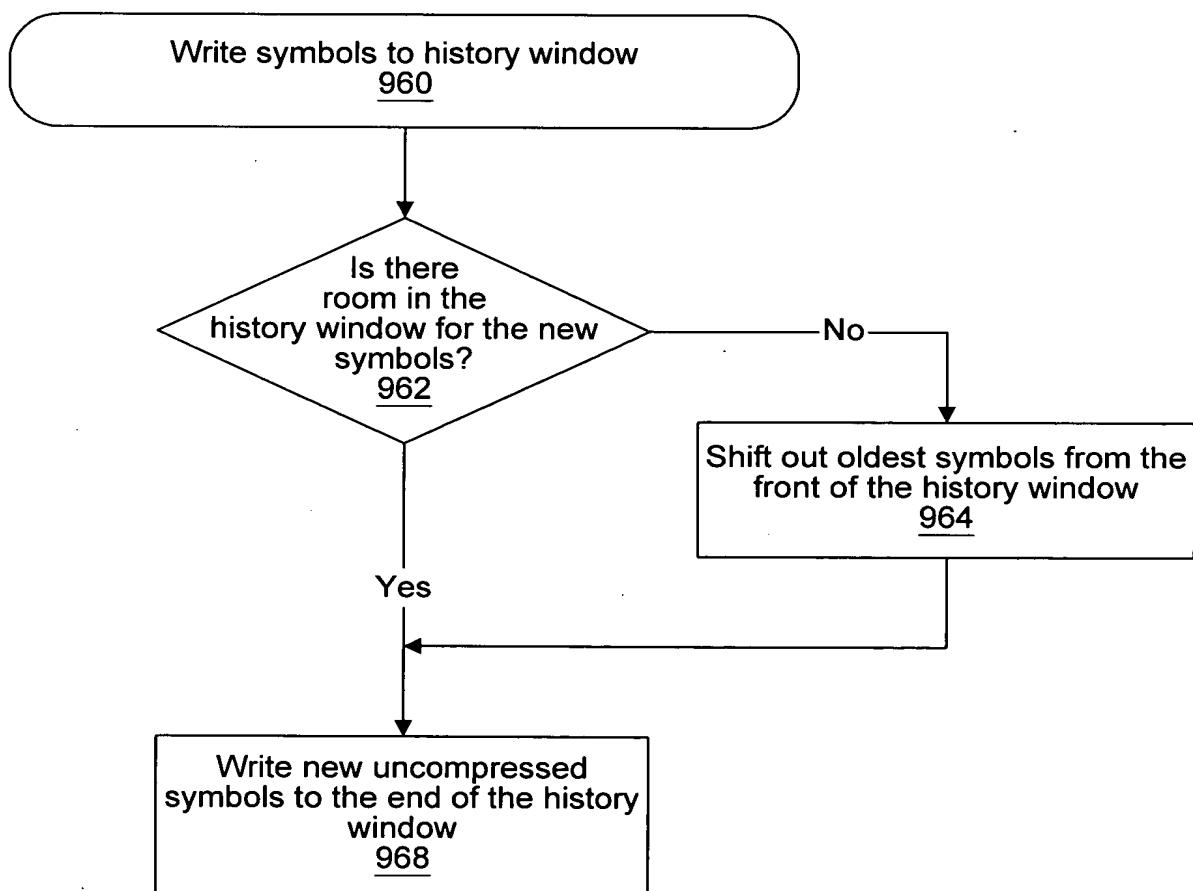


Decompression flowchart

Fig. 23d



Decompression flowchart

Fig. 23e

Decompression flowchart

Fig. 23f